Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.146”**

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**.146”**

**Top Material: Al**

**Backside Material: Au**

**Base: .012 x .112”**

**Emitter: .012 x .096”**

**Backside Potential: COLLECTOR**

**Mask Ref: 185**

**APPROVED BY: DK DIE SIZE .146” X .146” DATE: 10/4/21**

**MFG: SOLITRON THICKNESS .010” P/N: 2N3421**

**DG 10.1.2**

#### Rev B, 7/1